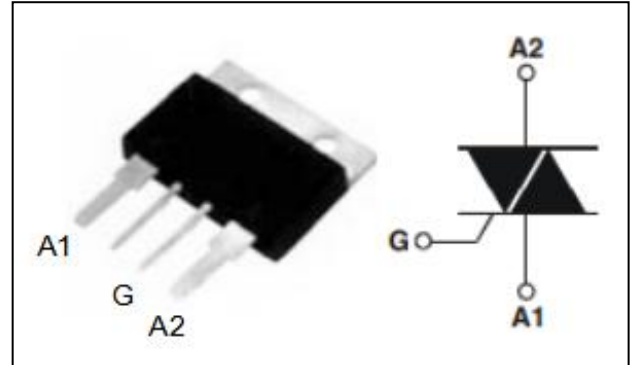


FEATURES

- NPNPN five-layer silicon bidirectional device
- With TO-P4 packaging
- Advanced technology to provide customers with high commutation performances

APPLICATIONS

- General purpose motor control circuits
- Home appliances
- Phase control operations in light dimmers and motor speed controllers

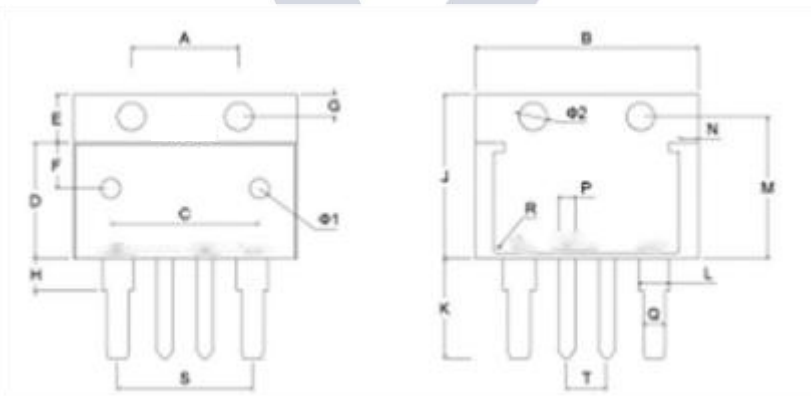


ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V _{DRM}	Repetitive Peak Off-state Voltage	800	V
V _{RRM}	Repetitive Peak Off-state Voltage	800	V
I _{T(RMS)}	Non Repetitive Surge Peak On-state Current (full sine wave, T _c = 80°C)	100	A
I _{TSM}	Non-repetitive Peak On-state Current f=60Hz	1000	A
I _{GM}	Peak Gate Current, t _p =20us, T _J = 125°C	8	A
I ² t	I ² t Value for Fusing (t=10ms)	6400	A ² S
P _{G(AV)}	Average Gate Power Dissipation T _J = 125°C	1	W
T _J	Operating Junction Temperature	125	°C
T _{stg}	Storage Temperature	-40~150	°C

ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	$V_R=V_{RRM}, T_J=125^\circ\text{C}$	2	mA
I_{DRM}	Repetitive peak off-state current	$V_D=V_{DRM}, T_J=25^\circ\text{C}$	10	μA
I_{GT}	Gate trigger current	I	$V_D=12\text{V}, R_L=30\ \Omega$	mA
		II		
		III		
		IV		
I_H	Holding current	$I_{GT}=500\text{mA}$	80	mA
V_{GT}	Gate trigger voltage all quadrant	$V_D=12\text{V}, R_L=30\ \Omega$	1.5	V
V_{TM}	On-state voltage	$I_T=82\text{A}, t_p=380\mu\text{s}$	1.55	V

PACKAGE OUTLINE
 Dimensions in mm


Dimension	Millimeter	
	Min	Max
A	15.43	16.76
B	32.00	32.30
C	21.30	21.90
D	16.65	17.98
E	6.83	8.06
F	6.60	7.00
G	3.15	3.45
H	4.57	5.44
J	24.06	25.38
K	14.16	15.36
L	4.47	5.23
M	20.76	22.10
N	2.68	3.43
P	2.35	2.77
Q	2.89	3.35
R	1.45	1.55
S	19.95	20.05
T	5.95	6.05
F 1	2.78	4.01
F 2	3.95	5.19

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